F rm PTO-1449 U.S.Department of Commerce Atty Docket 0756-2434 Serial No. 10/074,050 (Rev. 8-83) Patent and Trademark Office INFORMATION DISCEOSURE STATEMENT Applicants: Shunpei YAMAZAKI et al. APR 0 5 2002 Filing Date: February 14, 2002 Group Art Unit: N/A **U.S. PATENT DOCUMENTS** Examiner Document Number RAV Date Class Subclass Filing Date Initial (if appropriate) 5,529,937 06/25/1996 Zhang et al. 5,608,232 03/04/1997 Yamazaki et al. 5,639,698 06/17/1997 Yamazaki et al. 5.773.327 06/30/1998 Yamazaki et al. 5,814,540 09/29/1998 Takemura et al. 5,869,363 02/09/1999 Yamazaki et al. 5,888,858 03/30/1999 Yamazaki et al. 5,893,730 04/13/1999 Yamazaki et al 5,897,347 04/27/1999 Yamazaki et al. تت \overline{C} 5,915,174 06/22/1999 Yamazaki et al. CO 5,949,115 09/07/1999 Yamazaki et al. 5,956,579 09/21/1999 Yamazaki et al. 5,977,559 11/02/1999 Zhang et al. 5,985,740 11/16/1999 Yamazaki et al. 6,027,987 02/22/2000 Yamazaki et al. 6,048,758 04/11/2000 Yamazaki et al. 6,063,654 05/16/2000 Ohtani 6,066,518 05/23/2000 Yamazaki 6,071,764 06/06/2000 Zhang et al. 6,072,193 06/06/2000 Ohnuma et al. 6,077,731 06/20/2000 Yamazaki et al. 6,077,758 06/20/2000 Zhang et al. 6,084,247 07/04/2000 Yamazaki et al. 6,087,679 07/11/2000 Yamazaki et al. 6,093,934 07/25/2000 Yamazaki et al. 6,100,562 08/08/2000 Yamazaki et al. 6,111,557 08/29/2000 Koyama et al. 6,121,660 09/19/2000 Yamazaki et al. 6,133,073 10/17/2000 Yamazaki et al. Dh 6,133,075 10/17/2000 Yamazaki et al. Examiner Date Considered *EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this formwith next communication to applicant.

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<u>Translation</u> Yes No

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Complete if Known Substitute for form 1449A/PTO 10/074,050 Application Number (NFORMATION DISCLOSURE February 14, 2002 Filing Date STATEMENT BY APPLICANT First Named Inventor Shunpei YAMAZAKI et al. (use as many sheets as necessary) 2812 Group Art Unit Unassigned **Examiner Name** Attorney Docket Number 0756-2434

			U.S. PATENT DOCUMEN	re	
	1	U.S. Patent Document	U.S. FATENT DUCUMEN	Date of Publication of Cited	Pages, Columns, Lines, Where Relevant
Examiner Initials	Cite No. ¹	Number Kind Code ²	Name of Patentee or Applicant of Cited Document	Document MM-DD-YYYY	Passages or Relevant Figures Appear
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Substitute for form 1449A/PTO Complete if Known INFORMATION DISCLOSURE 10/074,050 **Application Number** February 14, 2002 Filing Date STATEMENT BY APPLICANT Shunpei YAMAZAKI et al. First Named Inventor 2 0 2002 (use as many sheets as necessary) 2812 Group Art Unit **Examiner Name** Unassigned of 3 Attorney Docket Number 0756-2434

	,			U.S. PATENT DOCUMEN	TS	
Examiner Cite No.1		U.S. Patent Document		Name of Patentee or Applicant of Cited	Date of Publication of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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INFORM	ATION DISC	LOSURE	Application Number	10/074,050	
			Filing Date	February 14, 2002	
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\$			Examiner Name	Unassigned	
Sheet 3	of	3	Attorney Docket Number	0756-2434	

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De		U.S. Patent Application Serial No. 10/051,064, "Semiconductor Device and Method of Manufacturing the Same", Takashi Hamada et al., January 18, 2002.	
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INFORMATION DISCLOSURE	Application Number	10/074,050
	Filing Date	February 14, 2002
10 203 STATEMENT BY APPLICANT	First Named Inventor	Shunpei YAMAZAKI et al.
(use as many sheets as necessary)	Group Art Unit	2812
TRADE Sheet 1 of 1	Examiner Name	Unassigned
Sheet 1 of 1	Attorney Docket Number	0756-2434

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		OTHER PRIOR	ART – NON PATENT LITE	RATURE DOCUMENTS				
Examiner Initials	Cite No.1		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.)., date, page(s), volume-issue number(s), publisher, city and/or country where published.					
M			O.J. Llewellyn et al., Implantation and Annealing of Cu in InP for Electrical Isolation: Microstructural Characterisation, IEEE, 1997, pages 313-316					
Dh.		Kevin S. Jones et al., Boron Diffusion Upon Annealing of Laser Thermal Processed Silicon, IEEE, 2000, pages 111-114						

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